



PATENT APPLICATION  
Docket N . 9898-204  
Client No. SS-16038-US

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yoon Sim and Jei-Hwan Yoo

Serial No. 09/901,930

Examiner: Terry D. Cunningham

Confirmation No. 7100

Filed: July 9, 2001

Art Unit: 2816

For: **NEGATIVE VOLTAGE GENERATOR FOR A SEMICONDUCTOR  
MEMORY DEVICE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☐ under 37 CFR 1.97 (b); or  
(Within three months of filing national application; or date of entry of International application;  
or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:  
☐ Statement under 37 CFR 1.97 (e), or  
☐ a \$180 fee under 37 CFR 1.17 (p); or  
(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)
- ☒ under 37 CFR 1.97 (d) together with:  
☐ Statement under 37 CFR 1.97 (e), and  
☒ a \$180.00 fee set forth in 37 CFR 1.17 (p).  
(Filed after final action or notice of allowance, whichever occurs first, but before payment of

the issue fee)

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with  
copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s)  
may be material to the examination of this application and for which there may be a duty to disclose in accordance  
with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A  
duplicate copy of this sheet is enclosed.

Customer No. 20575

12/16/2003 MAHME1 00000035 09901930

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Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

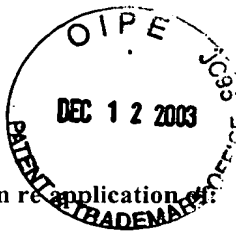
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Adrienne Chocholak



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**INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)**

**OTHER DOCUMENTS**

**Exam**

**Init**

**Ref**

**Author, Title, Date, Pertinent Pages, Etc.)**

\_\_\_\_\_      \_\_\_\_\_      Article from Journal of Solid-State Circuits, Vol. 24, No. 3, June 1989, page  
597-602. Entitled: A1-Mbit BiCMOS DRAM Using Temperature-Compensation  
Circuit Techniques.

**Examiner:** \_\_\_\_\_

**Date Considered:** \_\_\_\_\_

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